

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2968	257/777,778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 08:04
S2	290	257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 09:54
S3	4	((first second third fourth) adj3 frequenc\$3) and 257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 08:10
S4	1	((first second third fourth) adj2 frequenc\$3) and 257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 08:11
S5	9	((first second third fourth) with frequenc\$3) and 257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 08:12
S6	0	("multi-circuit" or (multipl\$2 adj circuit)) with frequenc\$3 and 257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 10:24
S7	0	("multi-circuit" or (multipl\$2 adj circuit)) same frequenc\$3 and 257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 08:17
S8	1	("multi-circuit" or (multipl\$2 adj circuit)) and 257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 08:17
S9	48	frequenc\$3 and circuit and 257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 08:24
S10	0	("multi-circuit" or (multipl\$2 adj circuit)) with frequenc\$3 and (flip adj chip)	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/30 08:40
S11	3823	("multi-circuit" or (multipl\$2 adj circuit))	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/30 08:40
S12	2	("multi-circuit" or (multipl\$2 adj circuit)) and flip adj chip	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/30 08:42
S13	11	("multi-circuit" or (multipl\$2 adj circuit)) and ("multi-chip" or (multipl\$2 adj chip))	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/30 08:42

S14	12	257/728.CCLS. and 257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 09:55
S15	4	257/686.CCLS. and 257/728.CCLS. and 257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/31 10:24
S16	51	("multi-circuit" or (multipl\$2 adj circuit)) same ("multi-chip" or (multipl\$2 adj chip)) and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 10:26
S17	1	("multi-circuit" or (multipl\$2 adj circuit)) same ("multi-chip" or (multipl\$2 adj chip)) same frequenc\$3 and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 10:27
S18	11	("multi-circuit" or (multipl\$2 adj circuit)) same ("multi-chip" or (multipl\$2 adj chip)) and frequenc\$3 and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 11:42
S19	33	("multi-circuit" or (multipl\$2 adj circuit) or (first adj circuit)) same ("multi-chip" or (multipl\$2 adj chip) or (first adj chip)) and frequenc\$3 and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 12:08
S20	2	("multi-circuit" or (multipl\$2 adj circuit) or (first adj circuit)) same ("multi-chip" or (multipl\$2 adj chip) or (first adj chip)) and ("multi-frequenc\$3" or (multipl\$2 adj frequenc\$3) or (first adj frequenc\$3)) and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 12:01
S21	31	("multi-chip" or (multipl\$2 adj chip) or (first adj chip)) same ("multi-frequenc\$3" or (multipl\$2 adj frequenc\$3) or (first adj frequenc\$3) or ((differ\$4 var\$3 chang\$3) adj frequenc\$3)) and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/30 12:04
S22	9	("2002/0079567").URPN.	USPAT	OR	ON	2005/08/31 09:37
S24	17	frequenc\$3 and 257/686.CCLS. and 257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/31 10:28
S25	633114	frequenc\$3 and noise interference disturbance and 257/686.CCLS. and 257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/31 10:29
S26	6	frequenc\$3 and (noise interference disturbance) and 257/686.CCLS. and 257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/31 10:40

S27	1	"20020024374"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/31 10:43
S28	31	"6358756"	US-PGPUB; USPAT; USOCR	OR	ON	2005/08/31 10:43
S29	847647	(board or module)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 08:49
S30	21780	(first second top bottom front back) adj2 chip	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 08:50
S31	21780	(first second top bottom front back) adj2 chip	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 08:51
S32	418696	(bond\$3 attach\$3 connect\$3) near2 (wire lead)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 08:55
S33	3847	S29 same S30	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 08:55
S34	3847	S31 same S33	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 08:56
S35	710	S32 same S34	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 08:56
S36	439	S35 same circuit	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 09:01
S37	6	S36 same frequenc\$3	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 09:16
S38	8235	(multipl\$5 differ\$3 vary\$3 vari\$4) with (chip substrate wafer silicon) with frequenc\$3	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 09:20
S39	76	(multipl\$5 differ\$3 vary\$3 vari\$4) adj2 (chip substrate wafer silicon) adj2 frequenc\$3	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 09:21
S40	15	(multipl\$5 differ\$3 vary\$3 vari\$4) adj (chip substrate wafer silicon) adj frequenc\$3	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 09:21
S41	11	(multipl\$5 differ\$3 vary\$3 vari\$4) adj (chip substrate wafer silicon) adj frequenc\$3 and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 09:22

S42	114	257/686.CCLS. and 257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 10:52
S43	56	chip and circuit and layout	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 10:53
S44	116	chip adj circuit adj (design layout)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/01 10:54
S45	114	257/686.CCLS. and 257/777.CCLS. and 257/778.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/02 09:02
S46	32	257/686.CCLS. and 257/777.CCLS. and amplif\$3 and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/02 09:29
S47	0	257/686.CCLS. and 257/777.CCLS. and "330".clas. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/02 09:30
S48	0	257/686.CCLS. and 257/777.CCLS. and "330".CLAS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/02 09:30
S49	0	257/686.CCLS. and "330".CLAS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/02 09:30
S50	14275	"330".CLAS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/02 09:30
S51	14275	"330".CLAS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/02 09:32
S52	1063	330/299,307,310,311.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/02 09:36
S53	20	330/299,307,310,311.CCLS. and ("multi-frequenc\$3" or (multipl\$2 adj frequenc\$3) or (first adj frequenc\$3)) and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/02 09:40
S54	668	330/310,311.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/02 09:41
S55	2	330/310,311.CCLS. and ("multi-chip" or (multipl\$2 adj chip) or (first adj chip)) and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/02 09:42
S56	0	"millions of circuits" and chip	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 07:37

S57	152	millions adj circuits and chip	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 07:38
S58	62576	power and amplifi\$5 and chip and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 07:42
S59	850	power adj amplifi\$5 with chip and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 07:43
S60	44	((power adj amplifi\$5) or (high adj frequency adj amplifi\$5)) with chip with (stack\$3 or packag\$3) and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 09:25
S61	399	((power adj amplifi\$5) or (high adj frequency adj amplifi\$5)) with chip with circuit and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 15:47
S62	0	((power adj amplifi\$5) or (high adj frequency adj amplifi\$5)) with chip with circuit with (hundred thousand million billion) and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 09:27
S63	11	((power adj amplifi\$5) or (high adj frequency adj amplifi\$5)) and chip with circuit with (hundred thousand million billion) and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 09:37
S64	77	amplifi\$5 and chip with circuit near2 (hundred thousand million billion) and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 09:44
S65	20	amplifi\$5 same chip with circuit near2 (hundred thousand million billion) and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 09:44
S66	3	((power adj amplifi\$5) or (high adj frequency adj amplifi\$5)) with chip with circuit and 330/66.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 15:52
S67	4	dual with amplifi\$5 with chip with circuit and 330/66,126.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/08 13:45
S68	1	("6188877").PN. OR ("6803817").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/08 12:52
S69	0	dual with amplifi\$5 with chip with circuit and 455/127.3,127.4.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/08 13:00
S70	1	dual with amplifi\$5 with chip and 455/127.3,127.4.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/08 13:00
S71	13	("6188877").URPN.	USPAT	OR	ON	2005/09/08 13:04

S72	1	multi with amplifi\$5 with chip with circuit and 330/66,126.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/08 13:48
S73	0	(multi-mode or multi-band) with amplifi\$5 with chip with circuit and 330/66,126.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/08 13:49
S74	0	(multi-mode or multi-band) with amplifi\$5 with chip and 330/66, 126.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/08 13:49
S75	6	(multi-mode or multi-band) with amplifi\$5 and chip and 330/66, 126.CCLS. and @ad<"20030326"	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/08 13:49